TD852 Series



DIP4, DC Input, Photo Darlington Transistor Coupler

Description

The TD852 series combine an AlGaAs infrared emitting diode as the emitter which is optically coupled to a silicon planar darlington phototransistor detector in a plastic DIP4 package with different lead forming options. With the robust coplanar double mold structure, TD852 series provide the most stable isolation feature.

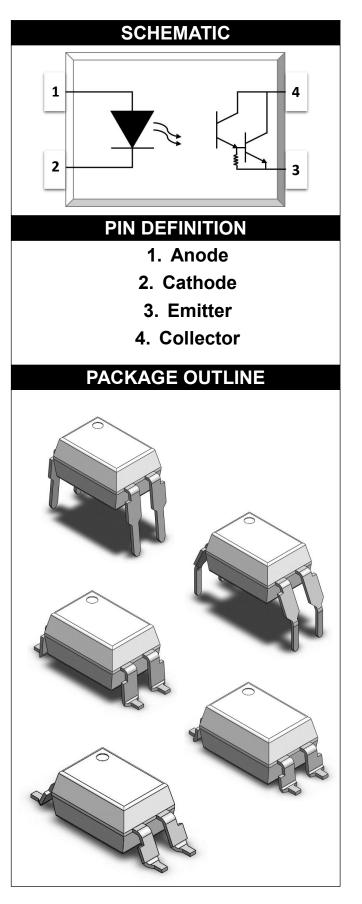
www.tdled.com

Features

- High isolation 5000 VRMS
- CTR flexibility available see order information
- DC input with transistor output
- Operating temperature range 55 °C to 110 °C
- REACH compliance
- Halogen free
- MSL class 1
- Regulatory Approvals
 - UL UL1577
 - VDE EN60747-5-5(VDE0884-5)
 - CQC GB4943.1, GB8898
 - cUL- CSA Component Acceptance
 Service Notice No. 5A

Applications

- Sequence controller
- Telephone/FAX
- System appliances, measuring instrument
- Programmable logic controller





www.tdled.comTD852SeriesDIP4, DC Input, Photo Darlington Transistor Coupler

ABSOLUTE MAXIMUM RATINGS										
PARAMETER	SYMBOL	VALUE	UNIT	NOTE						
INPUT										
Forward Current	IF	60	mA							
Peak Forward Current	I _{FP}	I _{FP} 1 A								
Reverse Voltage	VR	6	V							
Input Power Dissipation	Pı	100	mW							
OUTPUT										
Collector - Emitter Voltage	V _{CEO}	350	V							
Emitter - Collector Voltage	V _{ECO}	0.1	V							
Collector Current	Ιc	150	mA							
Output Power Dissipation	Po	150	mW							
COMMON										
Total Power Dissipation	Ptot	200	mW							
Isolation Voltage	Viso	5000	Vrms	2						
Operating Temperature	Topr	-55~100	°C							
Storage Temperature	Tstg	-55~125	°C							
Soldering Temperature	Tsol	260	°C							

Note 1. 100µs pulse, 100Hz frequency Note 2. AC For 1 Minute, R.H. = 40 ~ 60%

R LIGHTNING

www.tdled.com

www.tdled.comTD852SeriesDIP4, DC Input, Photo Darlington Transistor Coupler

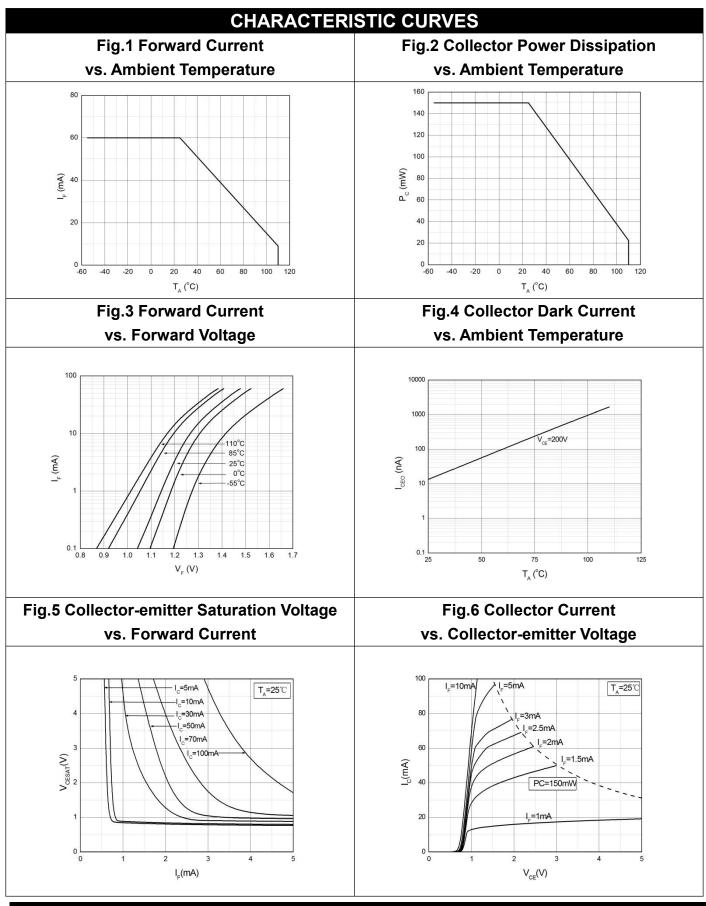
ELECTRICAL OPTICAL CHARACTERISTICS at Ta=25°C										
PARAMETER	SYMBOL	MIN	TYP.	MAX.	UNIT	TEST CONDITION	NOTE			
INPUT										
Forward Voltage	VF	-	1.24	1.4	V	IF=10mA				
Reverse Current	I _R	-	-	10	μA	VR=6V				
Input Capacitance	Cin	-	10	-	рF	V=0, f=1kHz				
OUTPUT										
Collector Dark Current	I _{CEO}	-	-	200	nA	VCE=200V, IF=0				
Collector-Emitter Breakdown Voltage	BV _{CEO}	350	-	-	V	IC=0.1mA, IF=0				
Emitter-Collector Breakdown Voltage	BV _{ECO}	0.1	-	-	V	IE=0.1mA, IF=0				
TRANSFER CHARACTERISTICS										
Current Transfer Ratio	CTR	1000	-	15000	%	IF=1mA, VCE=2V				
Collector-Emitter Saturation Voltage	V _{CE(sat)}	-	-	1.2	V	IF=20mA, IC=100mA				
Isolation Resistance	Riso	10^12	10^14	-	Ω	DC500V, 40 ~ 60% R.H.				
Floating Capacitance	CIO	-	0.6	1	рF	V=0, f=1MHz				
Response Time (Rise)	tr	-	3	18	μs	VCE=2V, IC=2mA	3			
Response Time (Fall)	tf	-	4	18	μs	RL=100Ω	3			
Cut-off Frequency	fc	-	80	-	kHz	VCE=2V, IC=2mA RL=100Ω,-3dB	4			

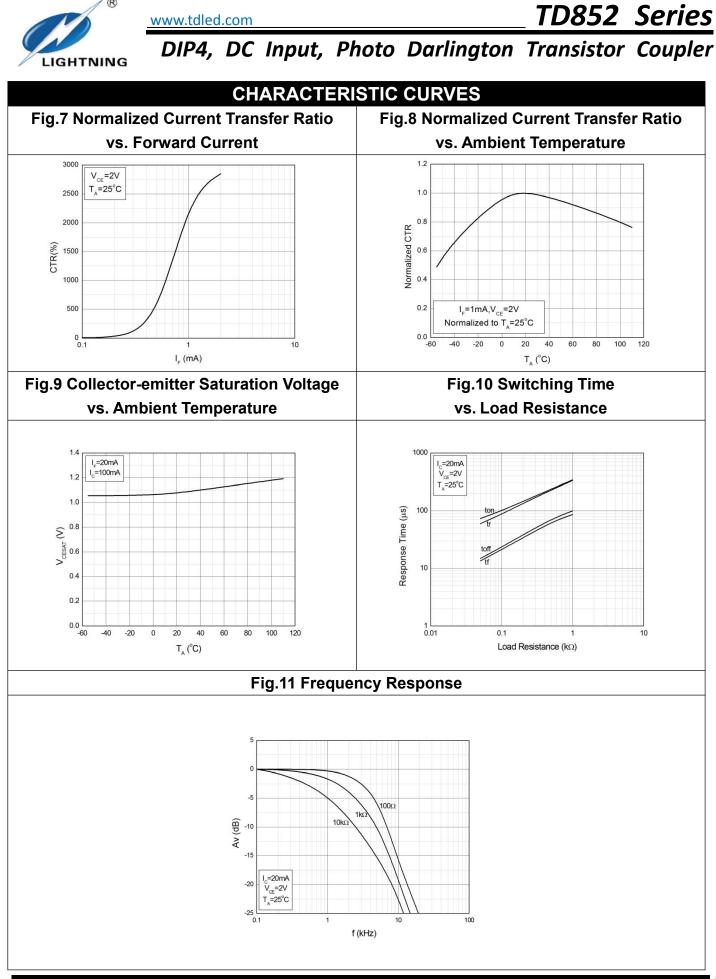
Note 3. Fig.12&13 Note 4. Fig.14

TD852 Series

RIGHTNING

www.tdled.com

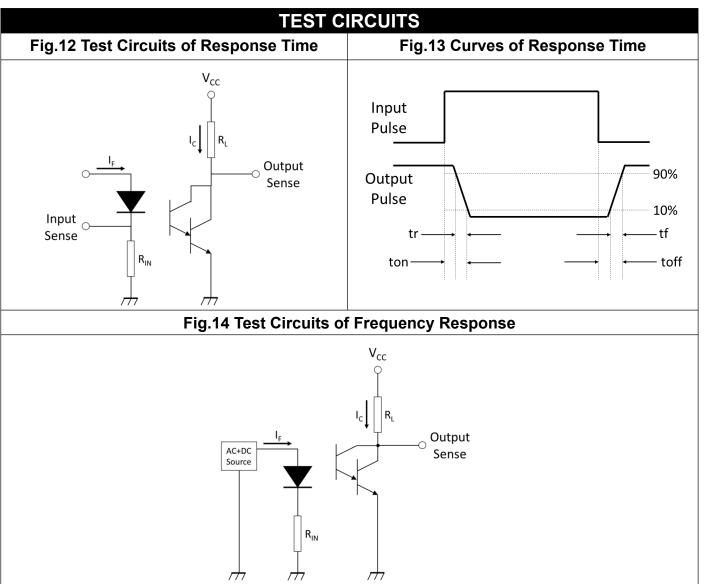




Document No: Preliminary

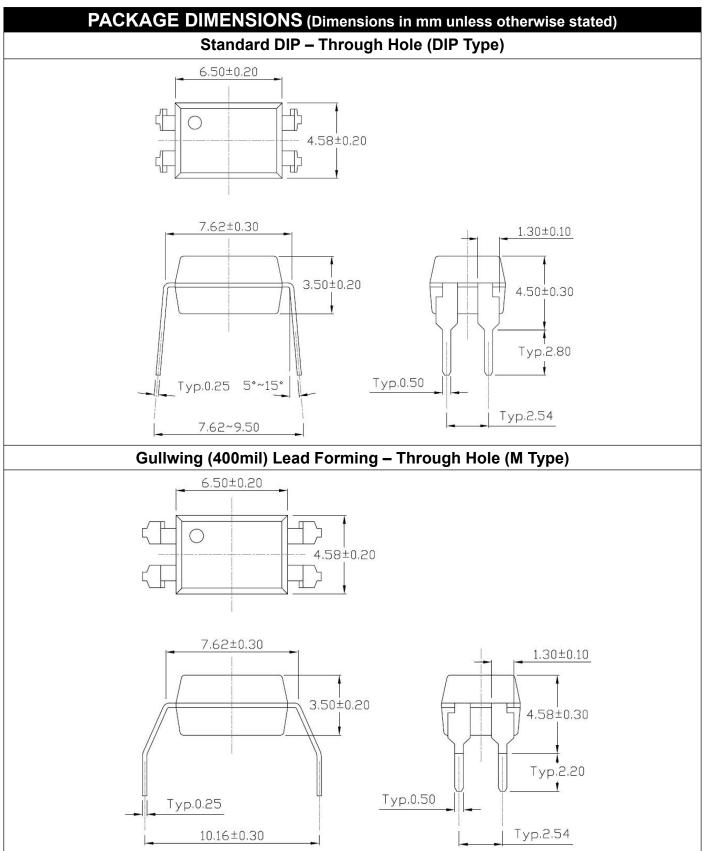


TD852 Series DIP4, DC Input, Photo Darlington Transistor Coupler

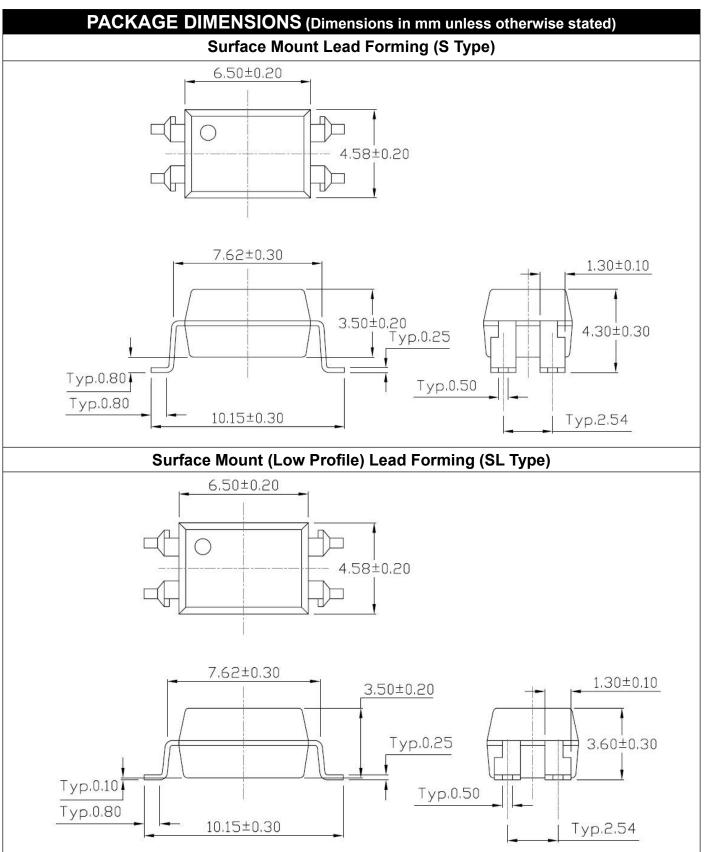


TD852 Series

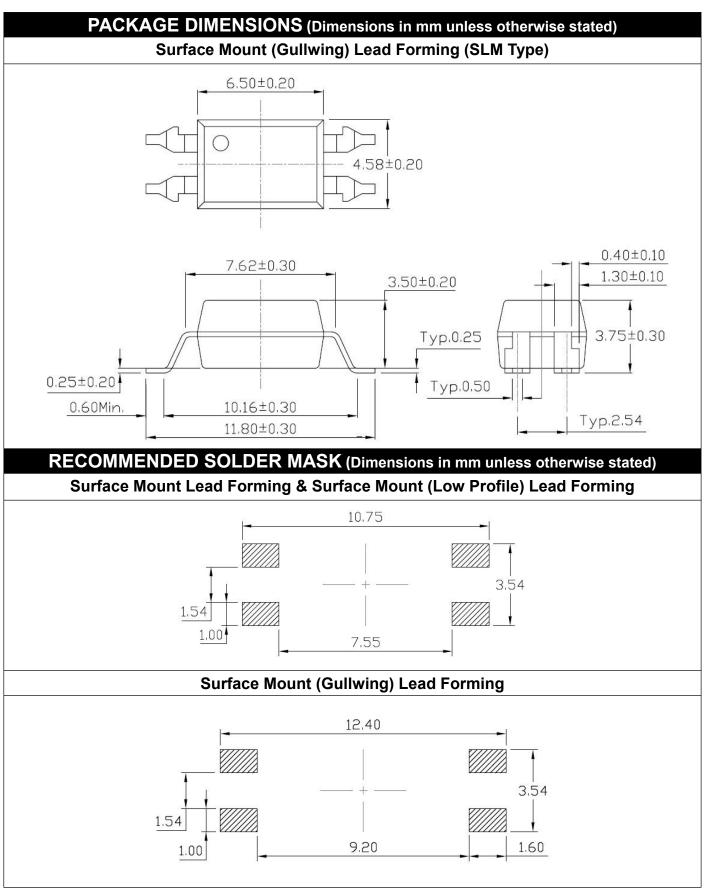
LIGHTNING DIP4,



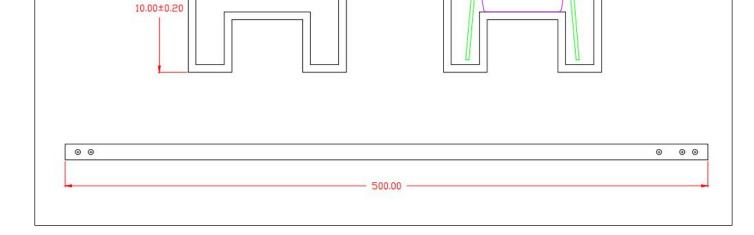




LIGHTNING

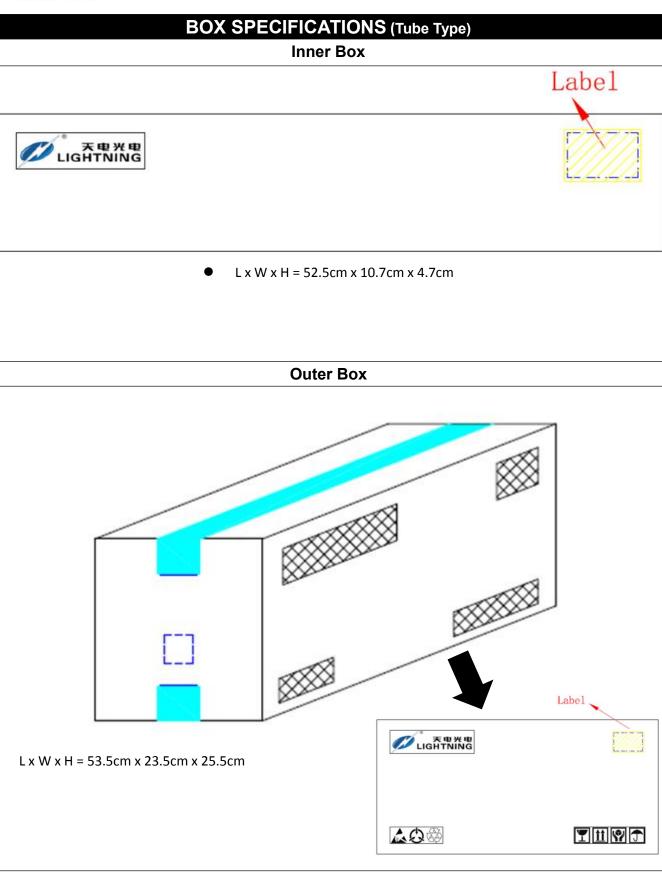


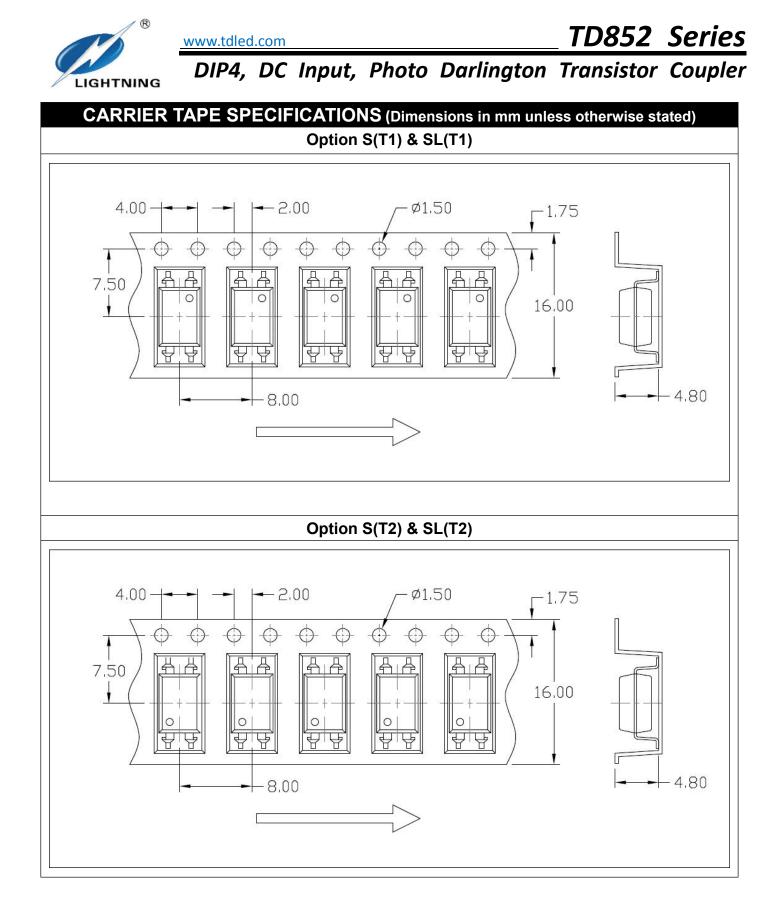






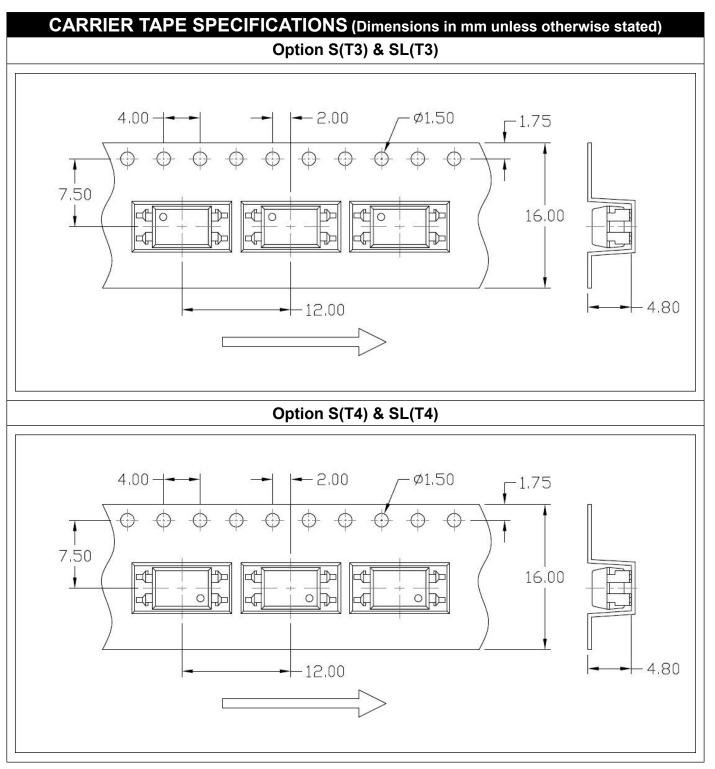
<u>_ TD852 Series</u> www.tdled.comTD852 SeriesDIP4, DC Input, Photo Darlington Transistor Coupler

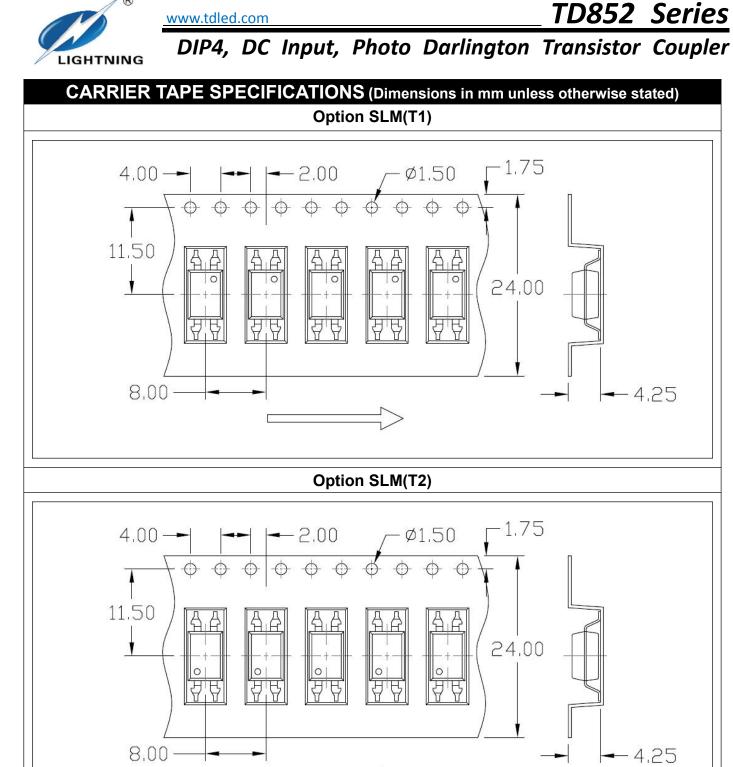


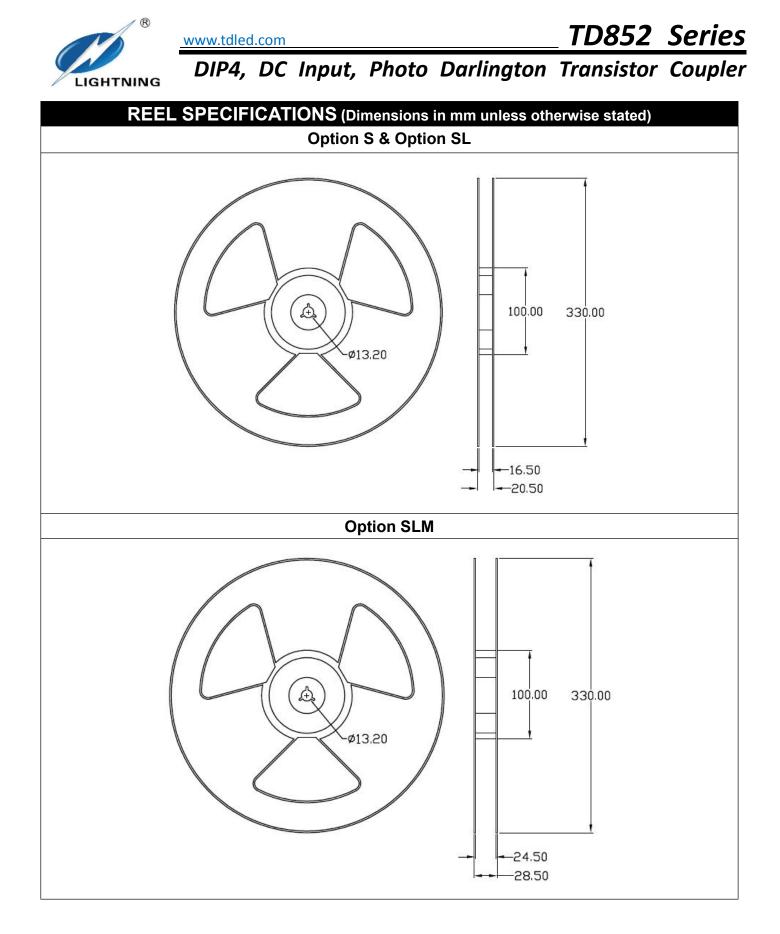








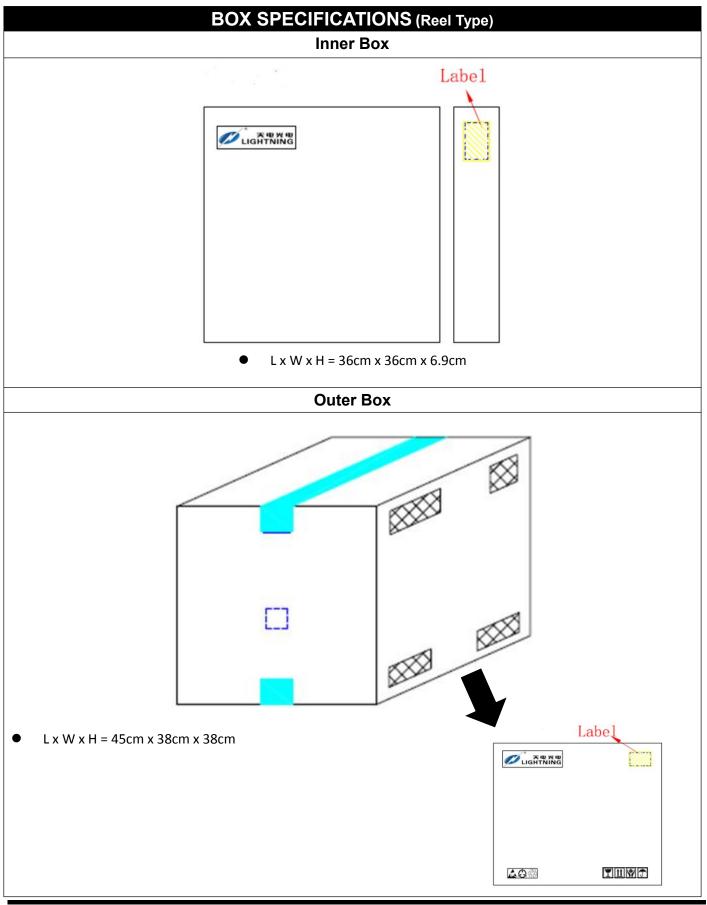






www.tdled.comTD852 SeriesDIP4, DC Input, Photo Darlington Transistor Coupler

TD852 Series







1500 Units/Reel

1000 Units/Reel

1000 Units/Reel

1000 Units/Reel

1000 Units/Reel

Μ

SL(T2)

SL(T3) SL(T4)

SLM(T1)

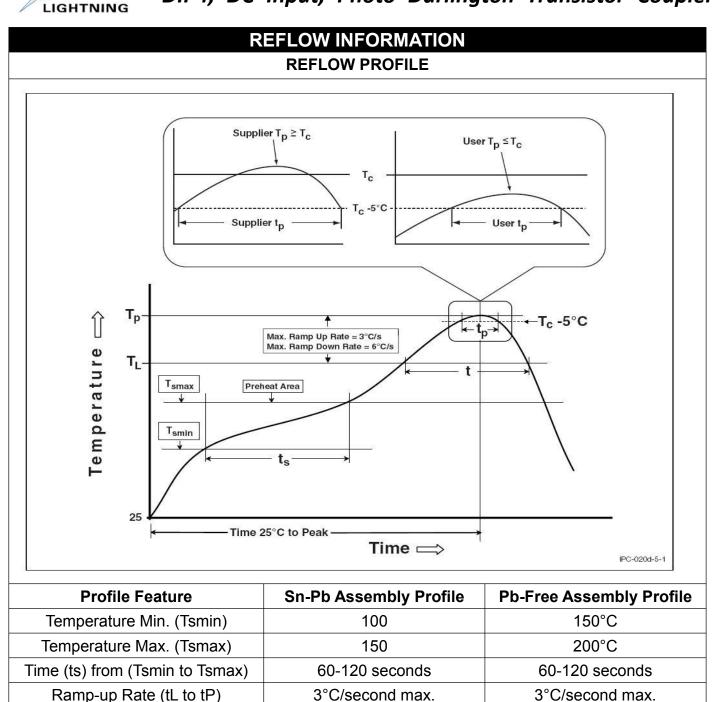
SLM(T2)

3 Reels/Inner box

5 Inner box/Outer box = 22.5k Units

5 Inner box/Outer box = 15k Units

DIP4, DC Input, Photo Darlington Transistor Coupler



183°C

60 - 150 seconds

235°C +0°C / -5°C

20 seconds

6°C/second max

6 minutes max.

Liquidous Temperature (TL)

Time (tL) Maintained Above (TL)

Peak Body Package Temperature Time (tP) within 5°C of 260°C

Ramp-down Rate (TP to TL)

Time 25°C to Peak Temperature

217°C

60 - 150 seconds

260°C +0°C / -5°C

30 seconds

6°C/second max 8 minutes max.



DIP4, DC Input, Photo Darlington Transistor Coupler

DISCLAIMER

- LIGHTNING is continually improving the quality, reliability, function and design. LIGHTNING reserves the right to make changes without further notices.
- The characteristic curves shown in this datasheet are representing typical performance which are not guaranteed.
- LIGHTNING makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, LIGHTNING disclaims (a) any and all liability arising out of the application or use of any product, (b) any and all liability, including without limitation special, consequential or incidental damages, and (c) any and all implied warranties, including warranties of fitness for particular
- The products shown in this publication are designed for the general use in electronic applications such as office automation, equipment, communications devices, audio/visual equipment, electrical application and instrumentation purpose, non-infringement and merchantability.
- This product is not intended to be used for military, aircraft, automotive, medical, life sustaining or lifesaving applications or any other application which can result in human injury or death.
- Please contact LIGHTNING sales agent for special application request.
- Immerge unit's body in solder paste is not recommended.
- Parameters provided in datasheets may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated in each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify LIGHTNING's terms and conditions of purchase, including but not limited to the warranty expressed therein.
- Discoloration might be occurred on the package surface after soldering, reflow or long-time use. It neither impacts the performance nor reliability.