

Description

The TDM501 series combine an AlGaAs infrared emitting diode as the emitter which is optically coupled to a silicon high speed photo transistor in a plastic SOP5 package.

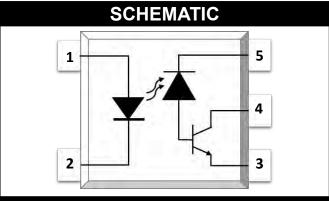
With the robust coplanar double mold structure, TDM501 series provide the most stable isolation feature.

Features

- High isolation 3750 VRMS
- DC input with high speed transistor
- Operating temperature range 40 °C to 100 °C
- REACH compliance
- Halogen free
- MSL class 1
- Regulatory Approvals
 - UL UL1577
 - VDE EN60747-5-5(VDE0884-5)
 - CQC GB4943.1, GB8898
 - cUL- CSA Component Acceptance
 Service Notice No. 5A

Applications

- Line receivers
- Telecommunication equipment
- Out interface to CMOS-LSTTL-TTL
- Wide bandwidth analog coupling
- Pulse transformer replacement
- Computer-peripheral interface

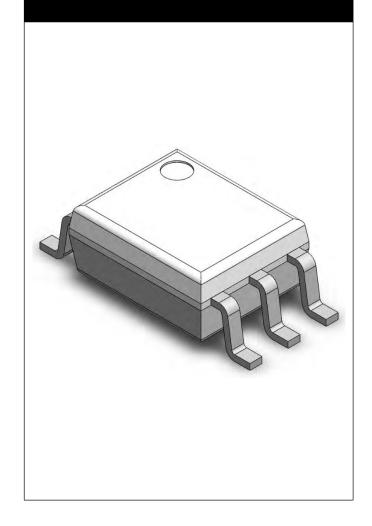


PIN DEFINITION

1.Anode 5.VCC

4.VO

2.Cathode 3.GND





ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	VALUE	UNIT	Note		
INPUT						
Forward Current	I _F	25	mA			
Peak Forward Current	I _{FP}	50	mA	1		
Peak Transient Current	I _{F(trans)}	1	Α	2		
Reverse Voltage	V_{R}	5	V			
Input Power Dissipation	Pı	100	mW			
OUTPUT						
Supply Voltage	V _{CC}	-0.5~30	V			
Output Voltage	Vo	-0.5~20	V			
Output Current	Io	8	mA			
Peak Output Current	Io	16	mA			
Output Power Dissipation	Po	100	mW			
COMMON						
Total Power Dissipation	Ptot	200	mW			
Isolation Voltage	Viso	3750	Vrms	3		
Operating Temperature	Topr	-40~100	°C			
Storage Temperature	Tstg	-55~125	°C			
Soldering Temperature	Tsol	260	°C	4		

Note 1. 50% duty, 1ms P.W

Note 2. ≤1μs P.W,300pps

Note 3. AC For 1 Minute, R.H. = $40 \sim 60\%$

Note 4. For 10 seconds





www.tdled.com TDM501 Series SOP5, 1Mbit/s High Speed Transistor Photo Coupler

ELECT						STICS at Ta=25°C	
PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	TEST CONDITION	NOTE
			INP	UT			
Forward Voltage	V_{F}	-	1.45	1.8	V	I _F =16mA	
Reverse Current	I _R	-	-	10	μΑ	V _R =5V	
Input Capacitance	Cin	-	60	-	pF	V=0, f=1MHz	
			OUT	PUT			
High Level Supply Current		_	0.01	1	μΑ	I _F =0mA, V _O =Open, V _{CC} =15V, Ta=25°C	
	Іссн	-	-	2	μA	I _F =0mA, V _O =Open, V _{CC} =15V	
Low Level Supply Current	I _{CCL}	_	200	-	μA	I_F =16mA, V_O =Open, V_{CC} =15V	
Logic High Output Current		-	0.001	0.5	μA	I _F =0mA, V _O =V _{CC} =5.5V, Ta=25°C	
	Іон	-	0.01	1	μA	I _F =0mA, V _O =V _{CC} =15V, Ta=25°C	
		-	-	50	μA	I _F =0mA, V _O =V _{CC} =15V	
TRANSFER	CHARACTI	ERISTI	CS(at T	a=0 to	70°C ,	unless specified otherwise)	1
Current Transfer CTR Ratio	CTD	20	-	-	0/	I_F = 16mA , V_O = 0.4V, V_{CC} =4.5V, Ta=25°C	
	CIR	15	-	-	- %	$I_F = 16mA , V_O = 0.5V,$ $V_{CC}=4.5V$	
Logic Low	\/	-	-	0.4	V	I_F = 16mA , I_O = 3mA, V_{CC} =4.5V, Ta=25°C	
Output Voltage	V _{OL}	-	-	0.5 V		$I_F = 16mA, I_O = 2.4mA,$ $V_{CC}=4.5V$	
Isolation Resistance	Riso	10^12	10^14	-	Ω	DC500V, 40 ~ 60% R.H.	
Floating Capacitance	C _{IO}	-	0.3	-	pF	V=0, f=1MHz	

Document No: Preliminary Release Date: 2021/9/28 Rev: v.0.5





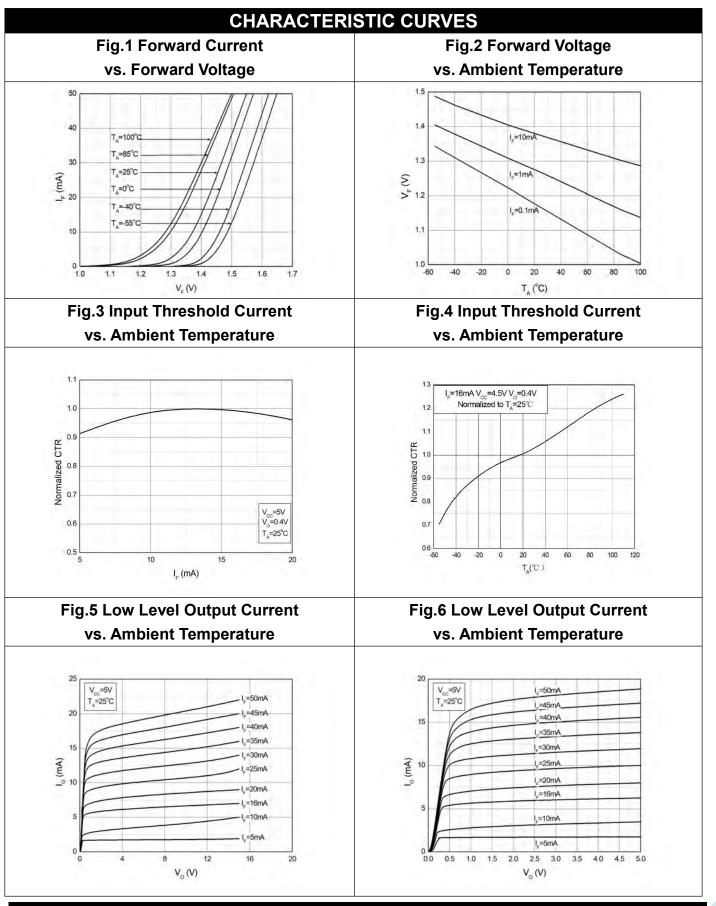
ELECTRICAL OPTICAL CHARACTERISTICS							
PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	TEST CONDITION	NOTE
SWITCHING CHARACTERISTICS(at Ta=0 to 70°C, I _F =16mA ,V _{CC} =5V, unless specified otherwise)							wise)
Propagation		_	0.4	0.8		R _L =1.9kΩ,T _A =25°C	
Delay Time	TPHL				μs	·	Fig.13
to Logic Low		-	-	1.0		R _L =1.9kΩ	
Propagation		_	0.35	0.8		R _L =1.9kΩ,T _A =25°C	
Delay Time	TPLH		0.00	0.0	μs	112 1131112, 1 1 2 3	Fig.13
to Logic High		-	-	1.0		$R_L=1.9k\Omega$	
Common Mode						L = 0m/ \/=1500\/nn	
Transient Immunity	СМн	15	-	-	kV/µs	$I_F = 0 \text{mA}$, $V_{CM} = 1500 \text{Vpp}$,	Fig.15
at Logic High						RL=1.9kΩ, T _A =25°C	
Common Mode						L = 16mA \/CM=1500\/nn	
Transient Immunity	CM _L	15	_	_	kV/µs	I_F = 16mA , VCM=1500Vpp, R_L =1.9kΩ, T_A =25°C	Fig.15
at Logic Low						ML-1.9K12, 1A-20 C	

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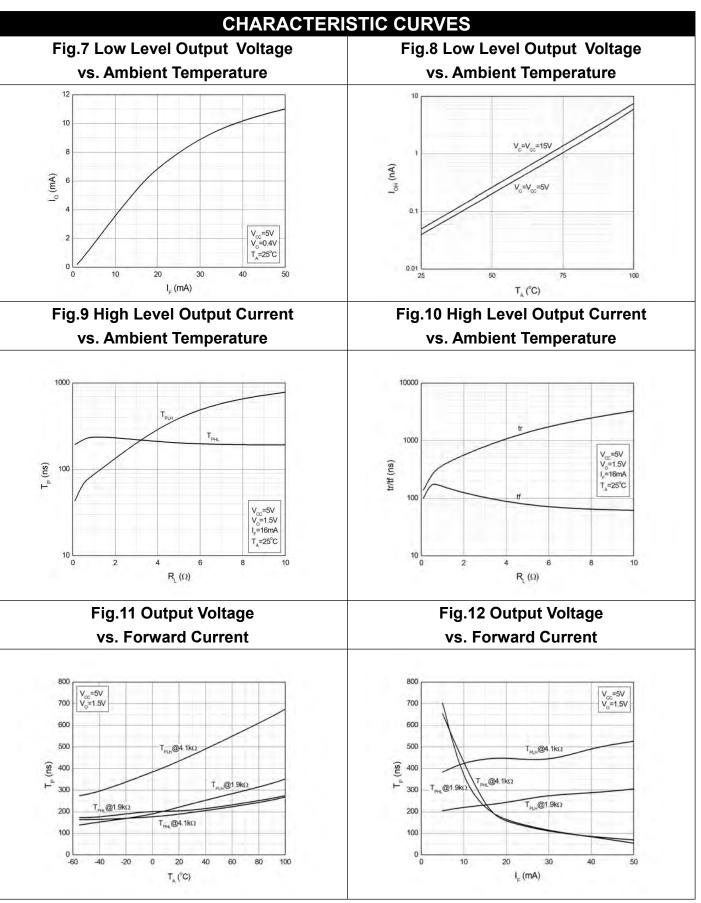
SOP5, 1Mbit/s High Speed Transistor Photo Coupler



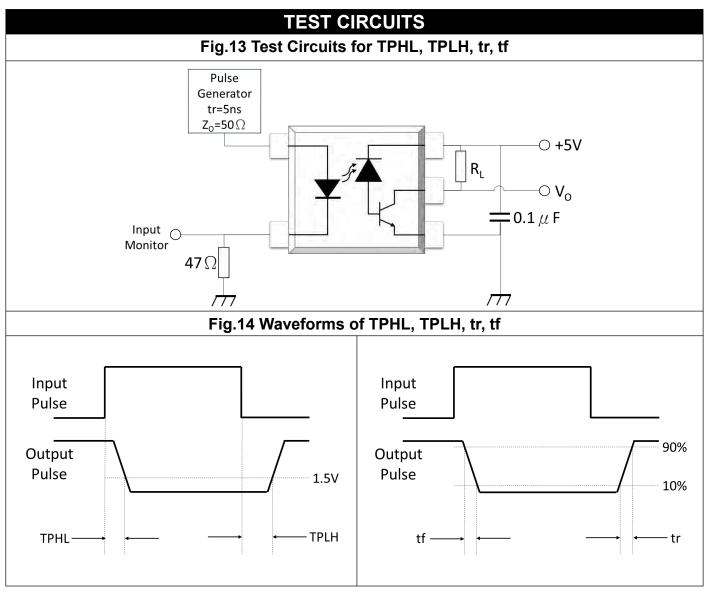
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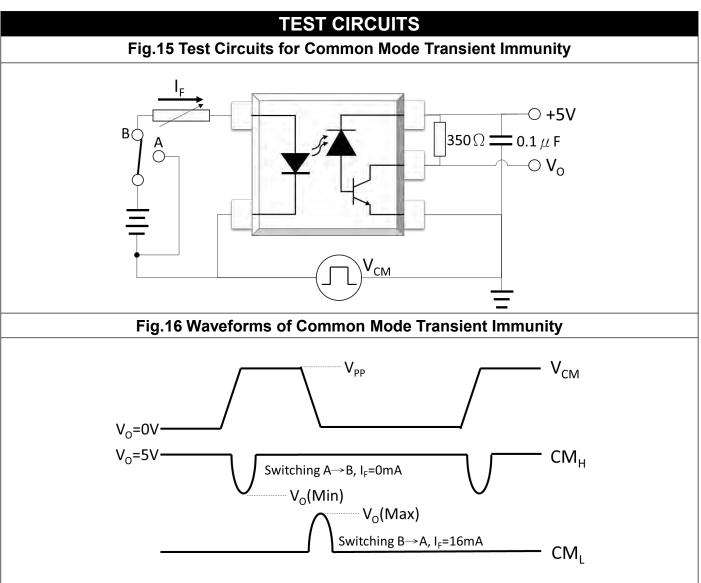




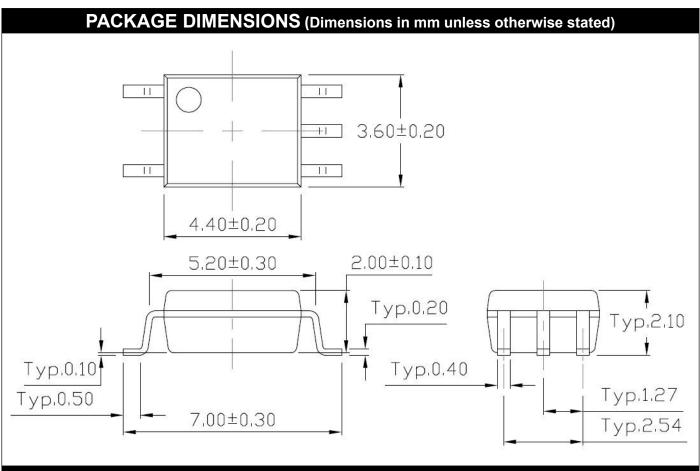




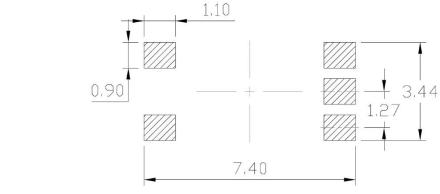








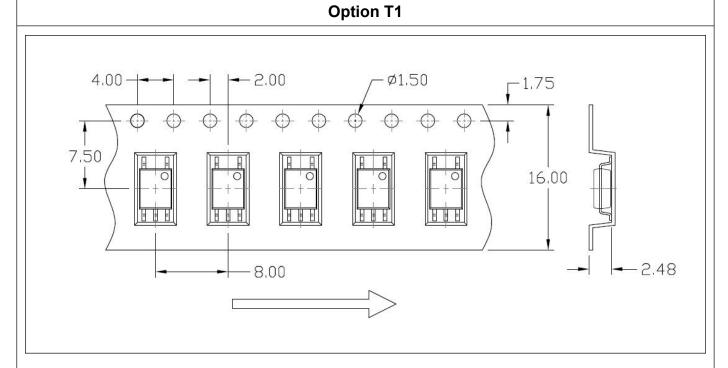
Recommended Solder Mask (Dimensions in mm unless otherwise stated)



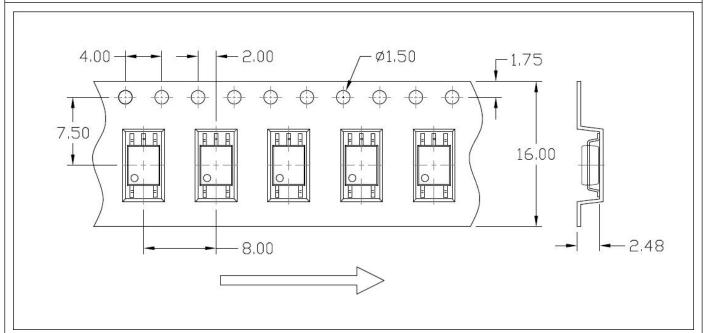




CARRIER TAPE SPECIFICATIONS (Dimensions in mm unless otherwise stated)



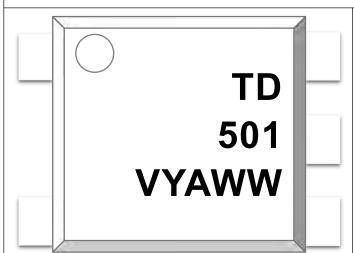
Option T2





ORDERING AND MARKING INFORMATION

MARKING INFORMATION



TD : Company Abbr.

501 : Part Number

V : VDE Option

Y : Fiscal Year

A : Manufacturing Code

WW : Work Week

ORDERING INFORMATION

TDM501(Z)-GV

TDM501 – Part Number

Z – Tape and Reel Option (T1/T2)

G – Material Option (G: Green)

V – VDE Option (V or None)



PACKING QUANTITY

171011110 907111111						
Option	Description	Quantity				
T1	Surface Mount Lead Forming – With Option 1 Taping	3000Units/Reel				
T2	Surface Mount Lead Forming – With Option 2 Taping	3000Units/Reel				

IPC-020d-5-1



SOP5, 1Mbit/s High Speed Transistor Photo Coupler

REFLOW INFORMATION REFLOW PROFILE Supplier T_p ≥ T_c User T_p ≤ T_c User T_p ≤ T_c User T_p ≤ T_c T_c

Profile Feature	Sn-Pb Assembly Profile	Pb-Free Assembly Profile
Temperature Min. (Tsmin)	100	150°C
Temperature Max. (Tsmax)	150	200°C
Time (ts) from (Tsmin to Tsmax)	60-120 seconds	60-120 seconds
Ramp-up Rate (tL to tP)	3°C/second max.	3°C/second max.
Liquidous Temperature (TL)	183°C	217°C
Time (tL) Maintained Above (TL)	60 – 150 seconds	60 – 150 seconds
Peak Body Package Temperature	235°C +0°C / -5°C	260°C +0°C / -5°C
Time (tP) within 5°C of 260°C	20 seconds	30 seconds
Ramp-down Rate (TP to TL)	6°C/second max	6°C/second max
Time 25°C to Peak Temperature	6 minutes max.	8 minutes max.



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- Please contact LIGHTNING sales agent for special application request.
- Immerge unit's body in solder paste is not recommended.
- Parameters provided in datasheets may vary in different applications and performance may vary
 over time. All operating parameters, including typical parameters, must be validated in each
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- Discoloration might be occurred on the package surface after soldering, reflow or long-time use. It neither impacts the performance nor reliability.